

Welcome to
a new dimension!



Introducing PMD[vision][®]

PMDTechnologies GmbH is a subsidiary of Audi Electronics Venture GmbH and ifm electronic gmbh. PMDTec is the world's leading supplier of integrated circuit technology for time-of-flight (ToF) imagers. We are a true leader – a market leader, solutions leader and technology leader. And we hold more than 25 worldwide patents for 3D ToF technology, with a further 40 pending.

Our business is the development and commercialization of smart optical systems for 3D range measurement. We provide innovative solutions for automotive, consumer, industrial automation, robotics and medical applications. The key task of our technical development is a consistent improvement of our PMD technology (Photonic Mixer Device), a revolutionary new generation of smart optical CMOS sensors.

For the future we have set ourselves ambitious goals. To achieve them, we want to strengthen our ASIC design team. We offer open position as:

Digital Design Engineer for 2D/3D Image Sensors

Job specification:

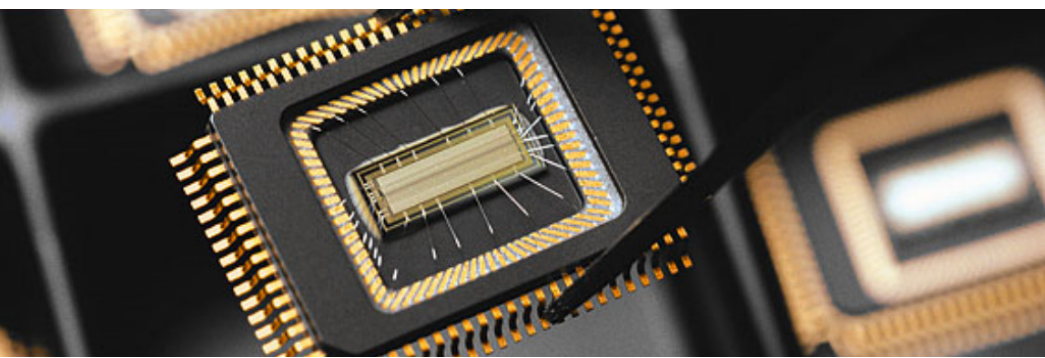
As Digital Design Engineer for 2D/3D Image Sensors you develop SoC-ASICs for 3D range measurement (3D-ToF). You are able to define and specify new ASIC products based on the demands of customers and applications. You implement synthesizable VHDL cores for i.e. on-chip signal processing, algorithms, sequence controls and I/O-interfaces. To ensure complete functionality in compliance with the specification you use digital/mixed-signal simulator tools for verification of the complete system. The consistent realization of an integral design-for-test strategy assures the testability with high error coverage in series production. You independently evaluate your designs using modern testing equipment and give support during product qualification and ramp-up of production.

Requirement profile:

- M.Sc. Microelectronics / Electrical Engineering or equivalent
- Minimum of five years of work experience
- Strong knowledge in digital CMOS ASIC design, beneficial experience in CMOS image sensor design
- Proficient in digital/mixed-signal EDA tools, preferably Cadence (Virtuoso Digital Implementation, IUS, Encounter RTL Compiler, SoC Encounter, AMS Designer)
- Strong VHDL-programming skills, preferably additional experience with a scripting-language like PERL or TCL
- Experience in wafer-level characterisation/qualification and data analysis
- Profound analytical skills
- Independent and accurate working methodology
- Team player and good communication skills

If you are ready to join our development team, please send your application containing a possible starting date and your salary requirement to:

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A global player – with a
passion for 3D imaging.